

FIG. 2

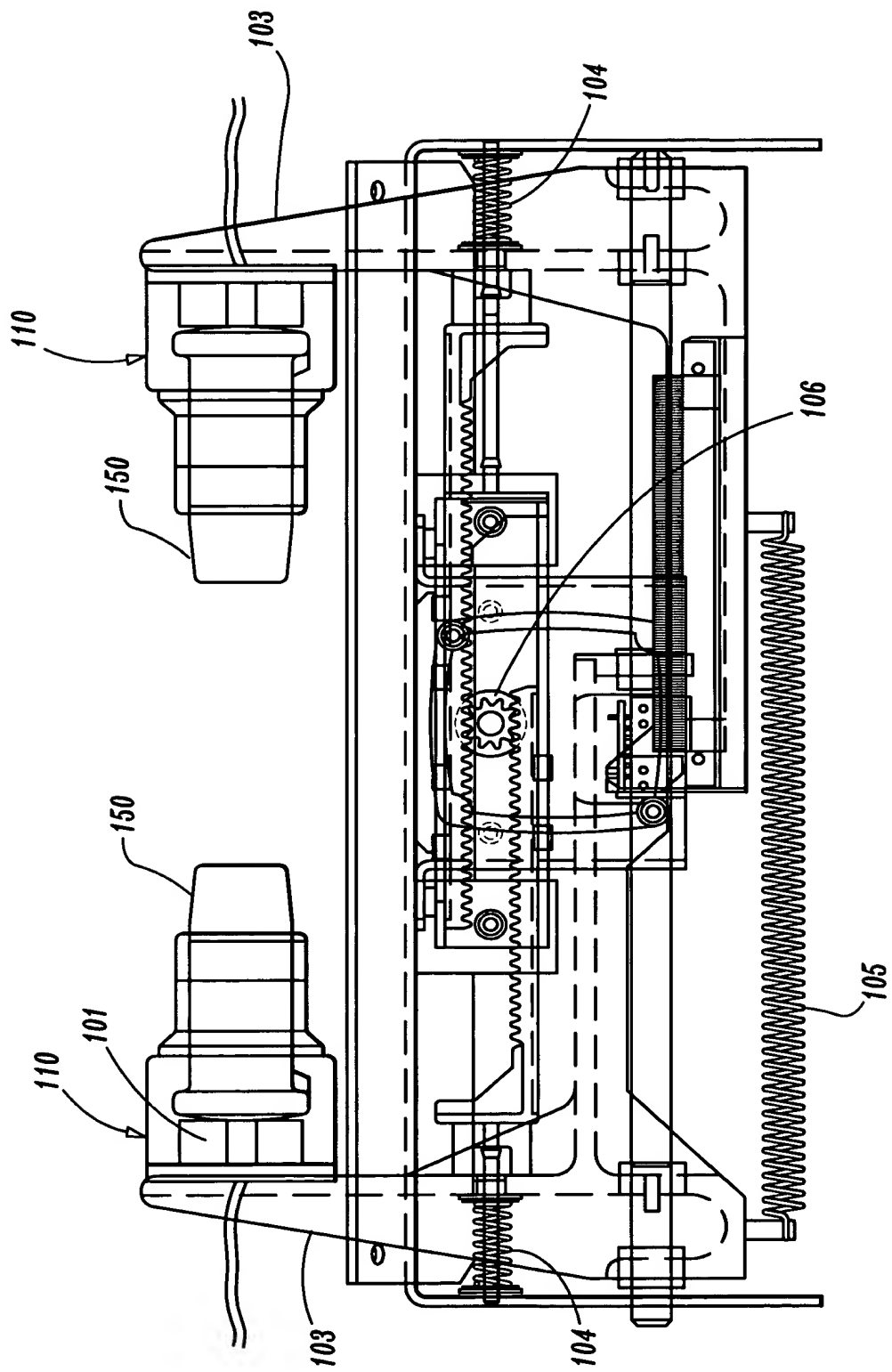


FIG. 3

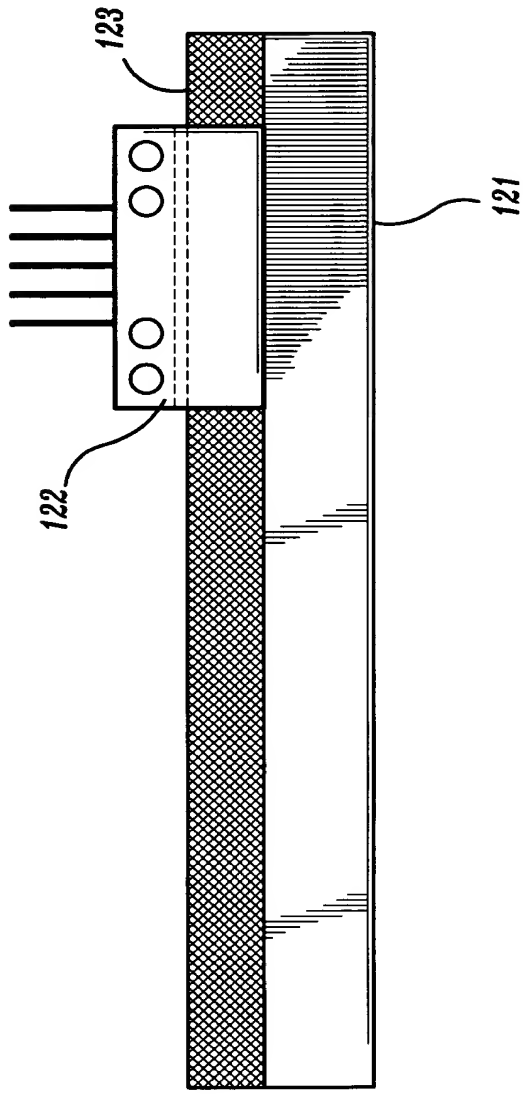


FIG. 4A

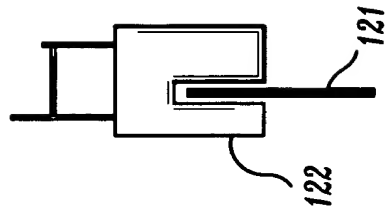


FIG. 4B

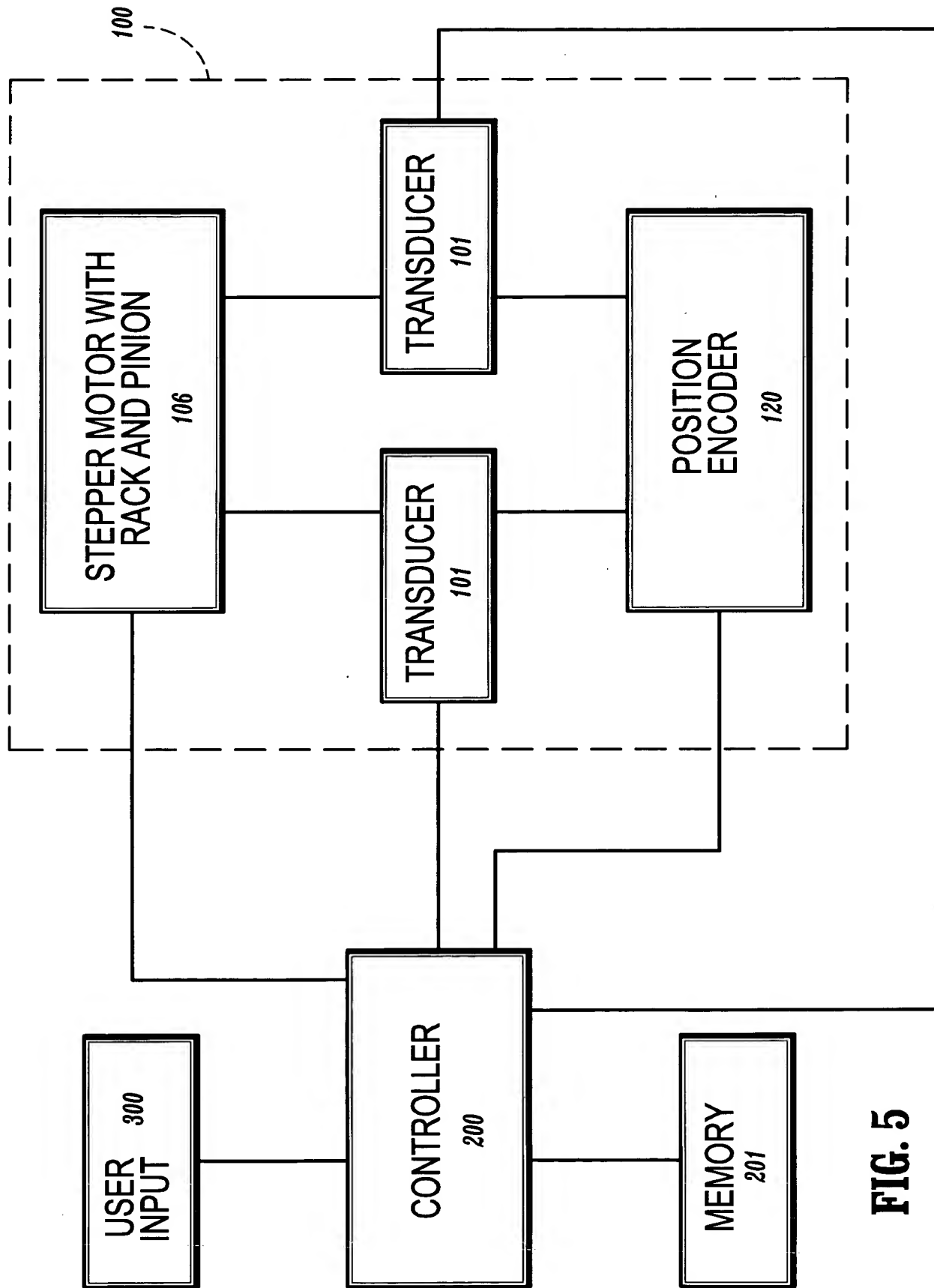


FIG. 5

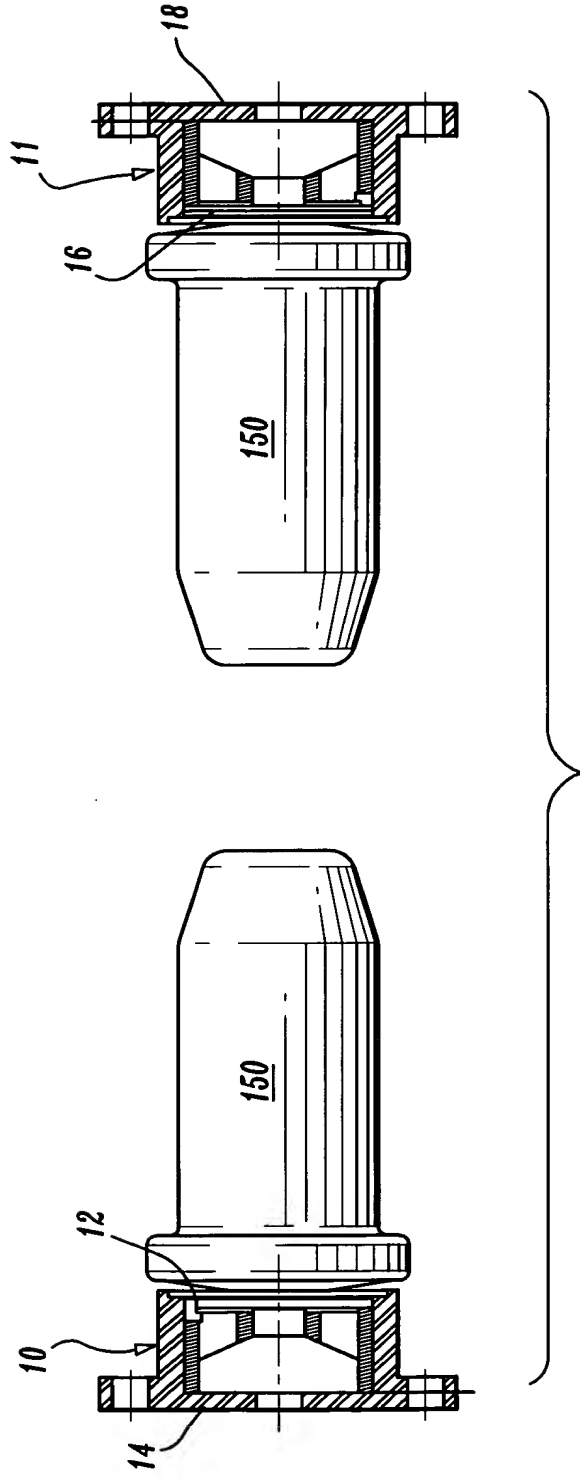


FIG. 6

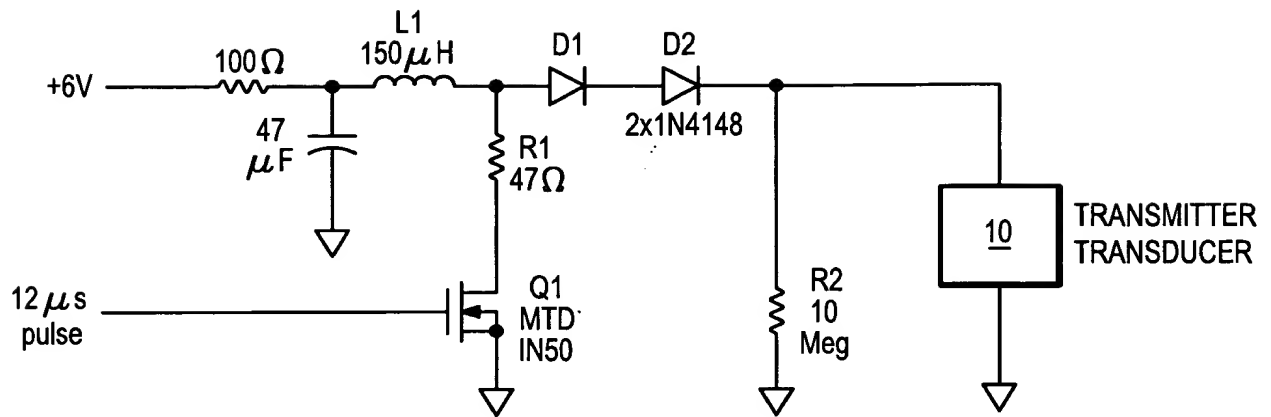


FIG. 7A

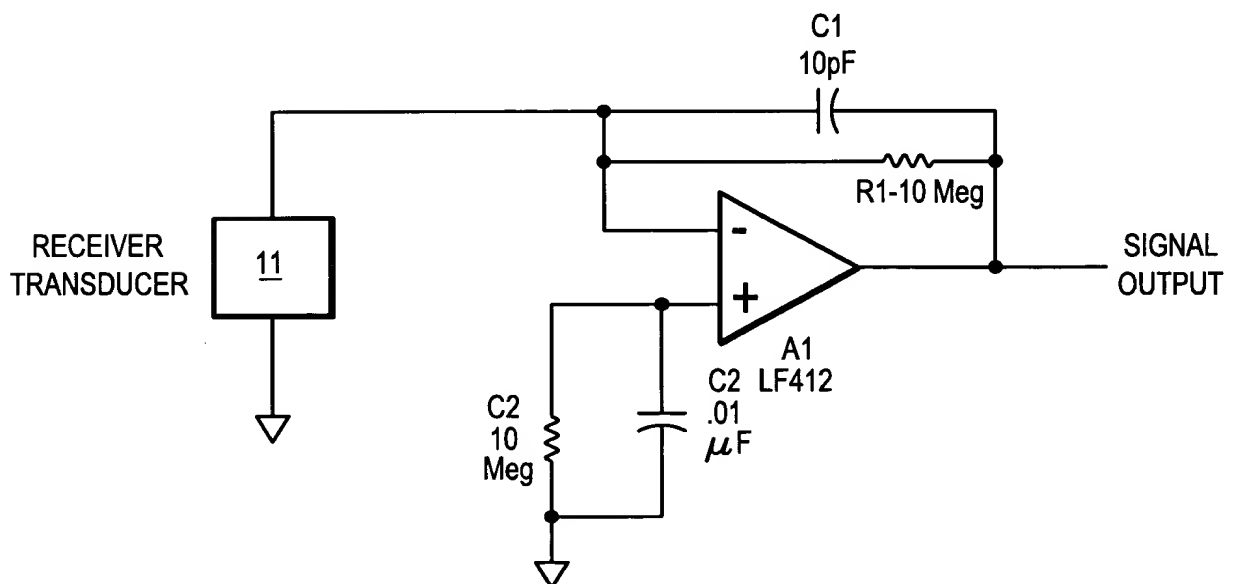


FIG. 7B

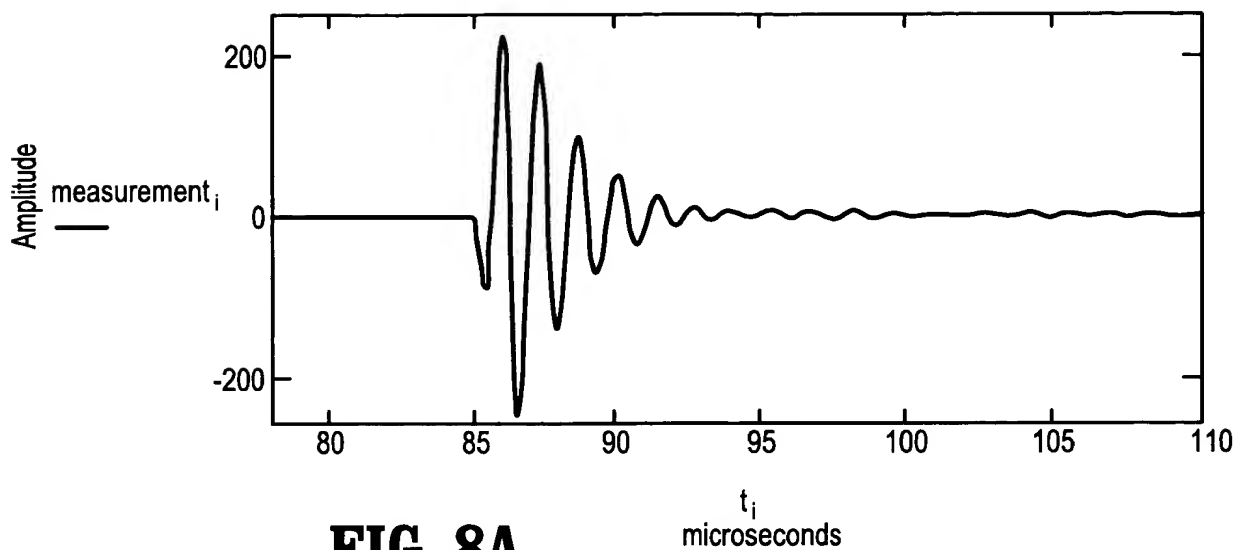


FIG. 8A

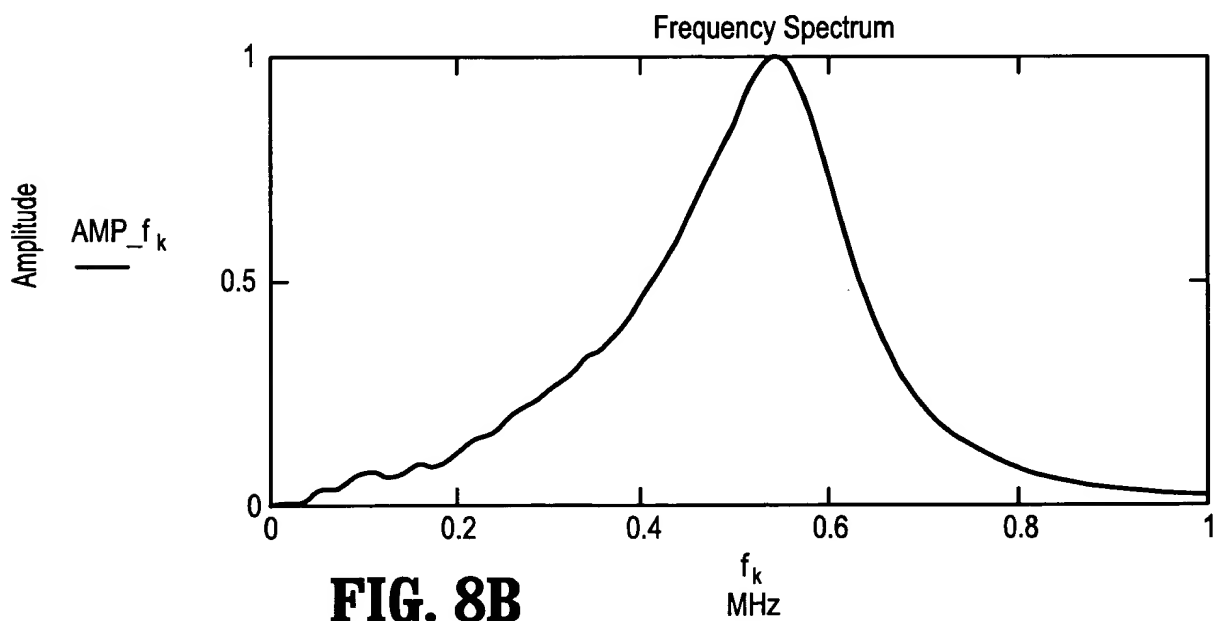


FIG. 8B

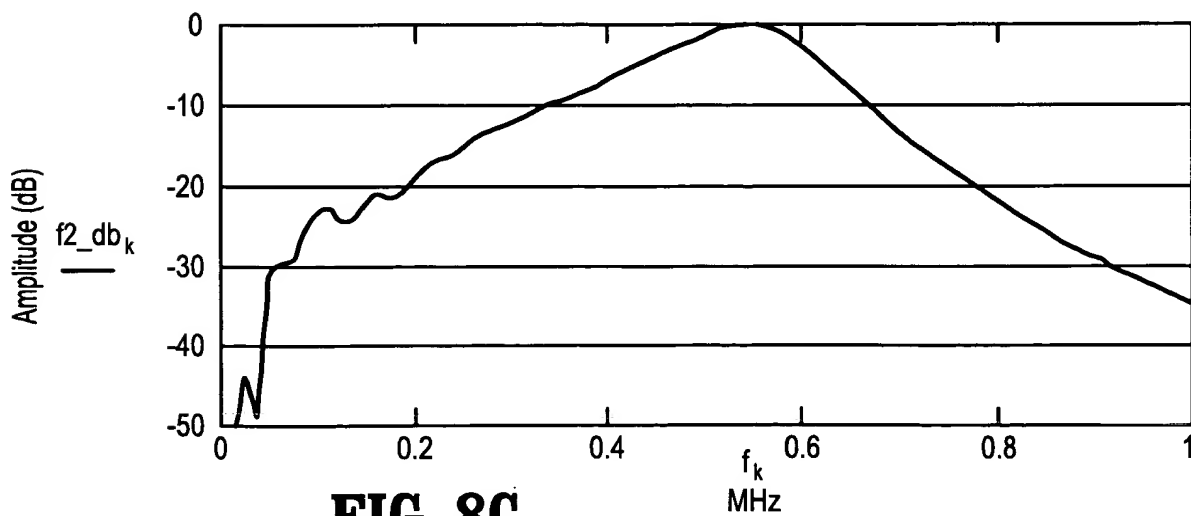


FIG. 8C

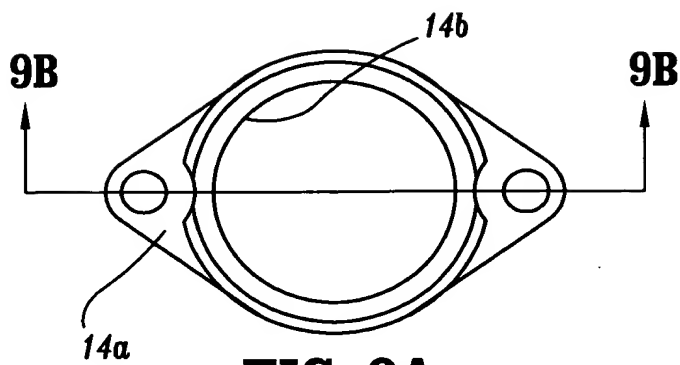


FIG. 9A

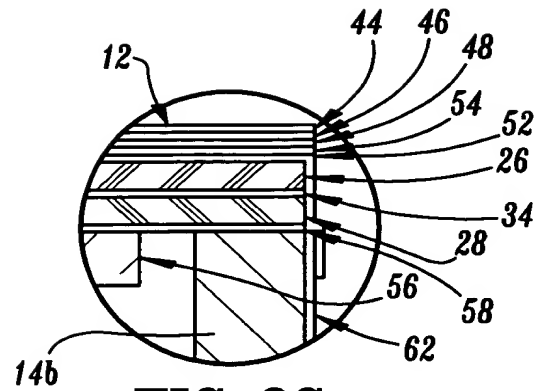


FIG. 9C

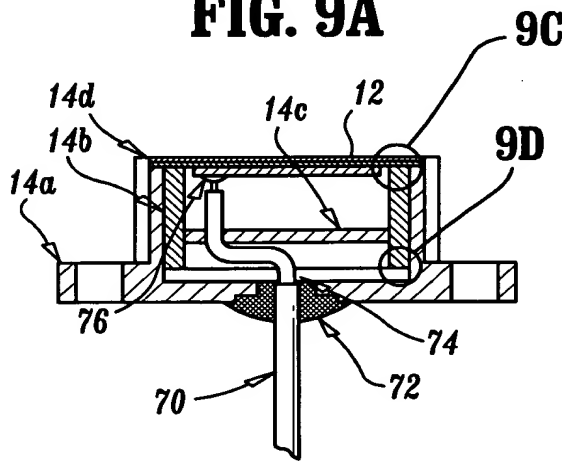


FIG. 9B

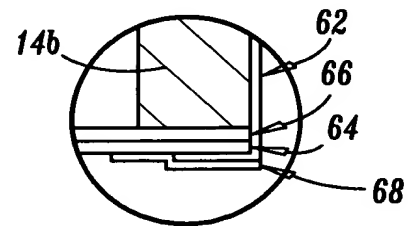


FIG. 9D

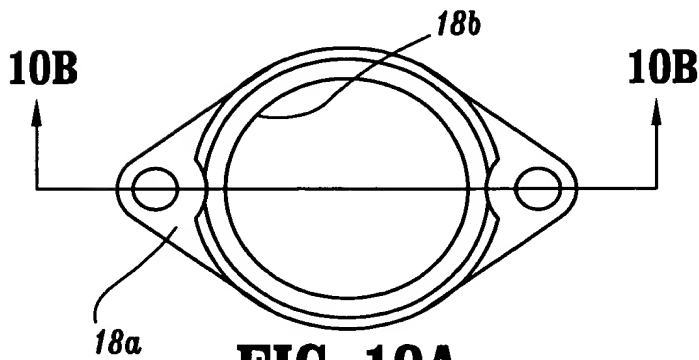


FIG. 10A

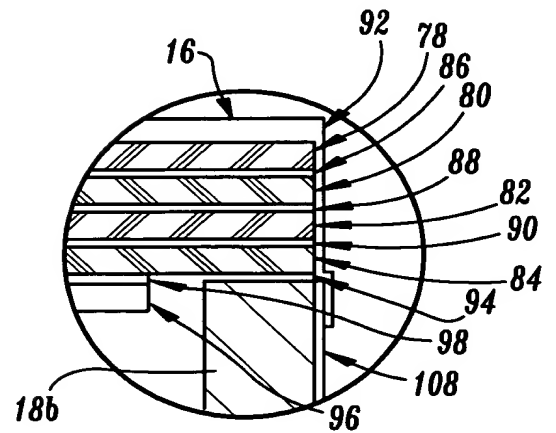


FIG. 10C

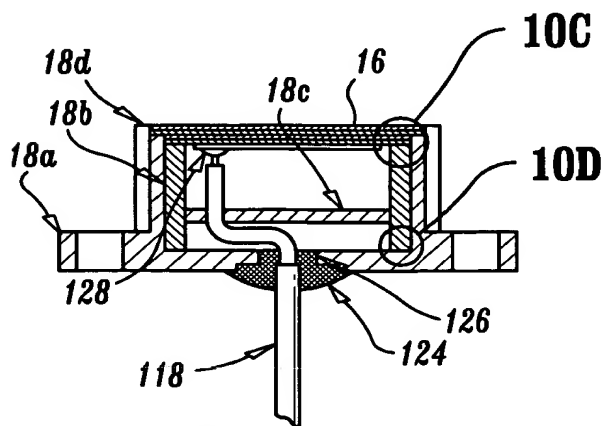


FIG. 10B

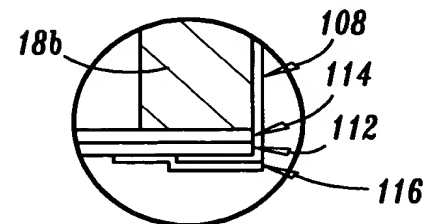


FIG. 10D

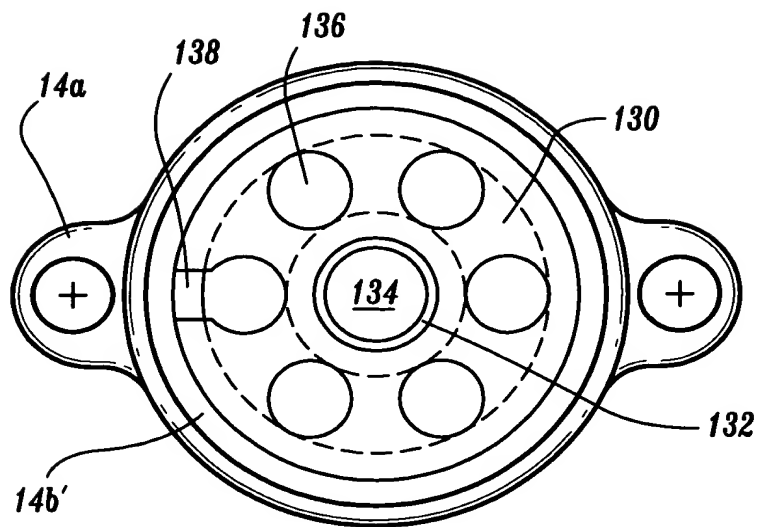


FIG. 13

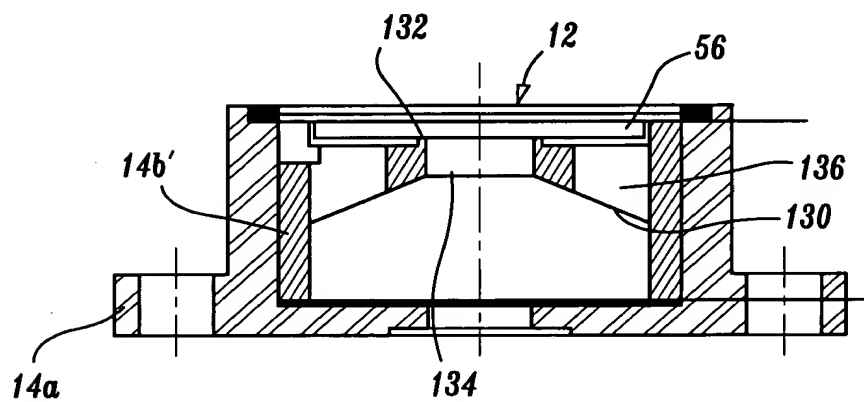


FIG. 11

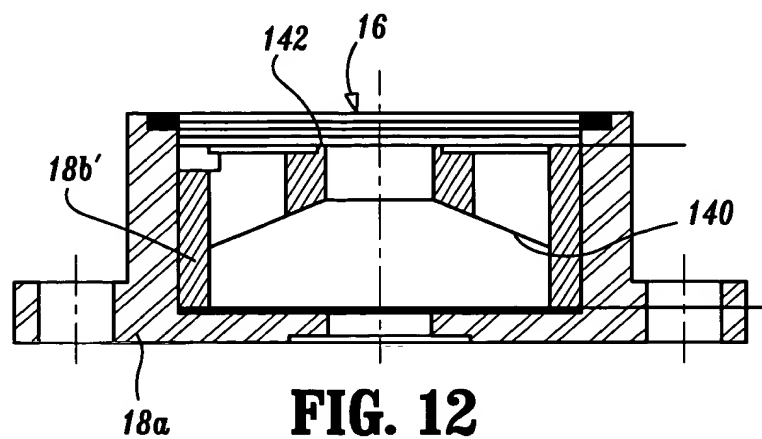


FIG. 12

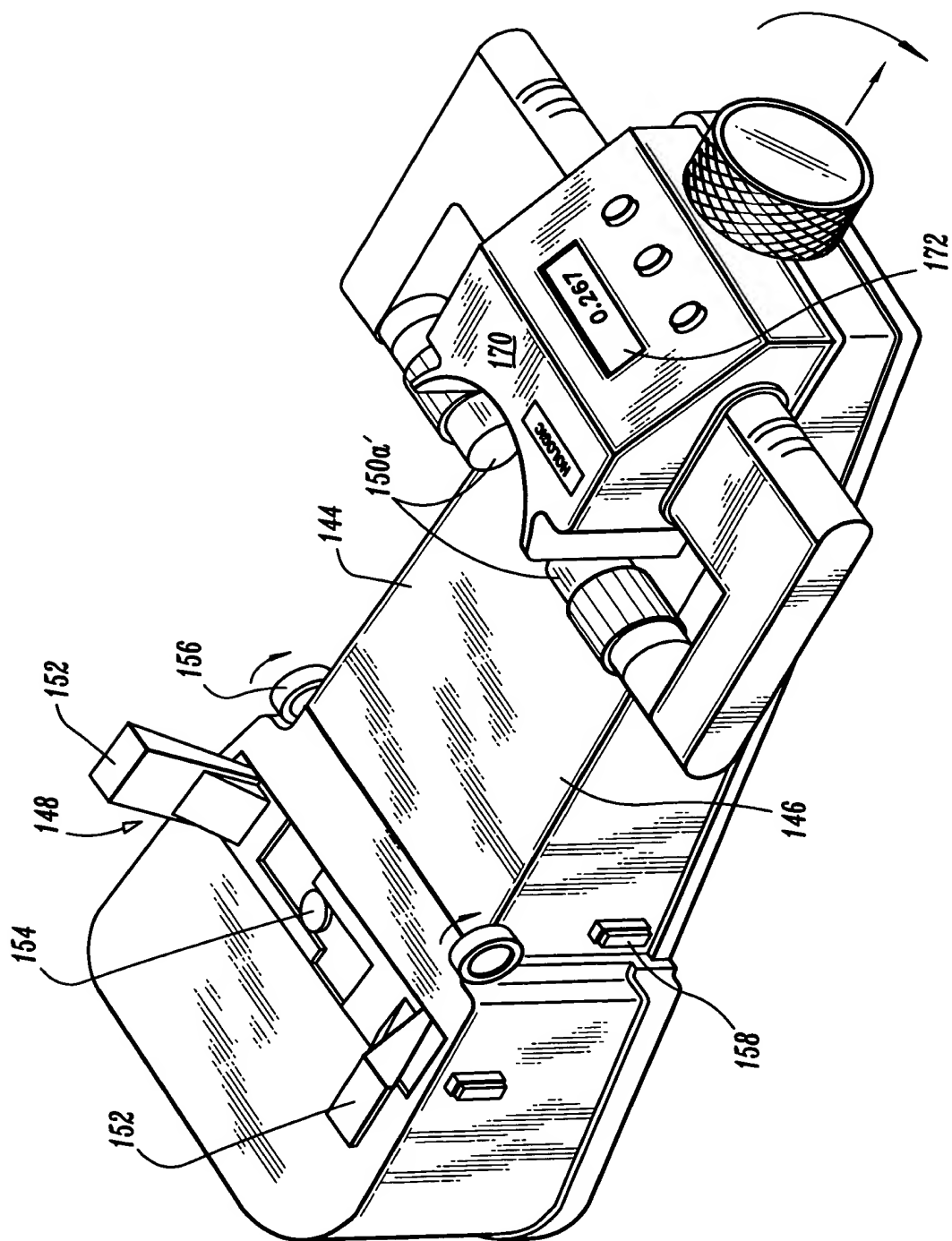


FIG. 14

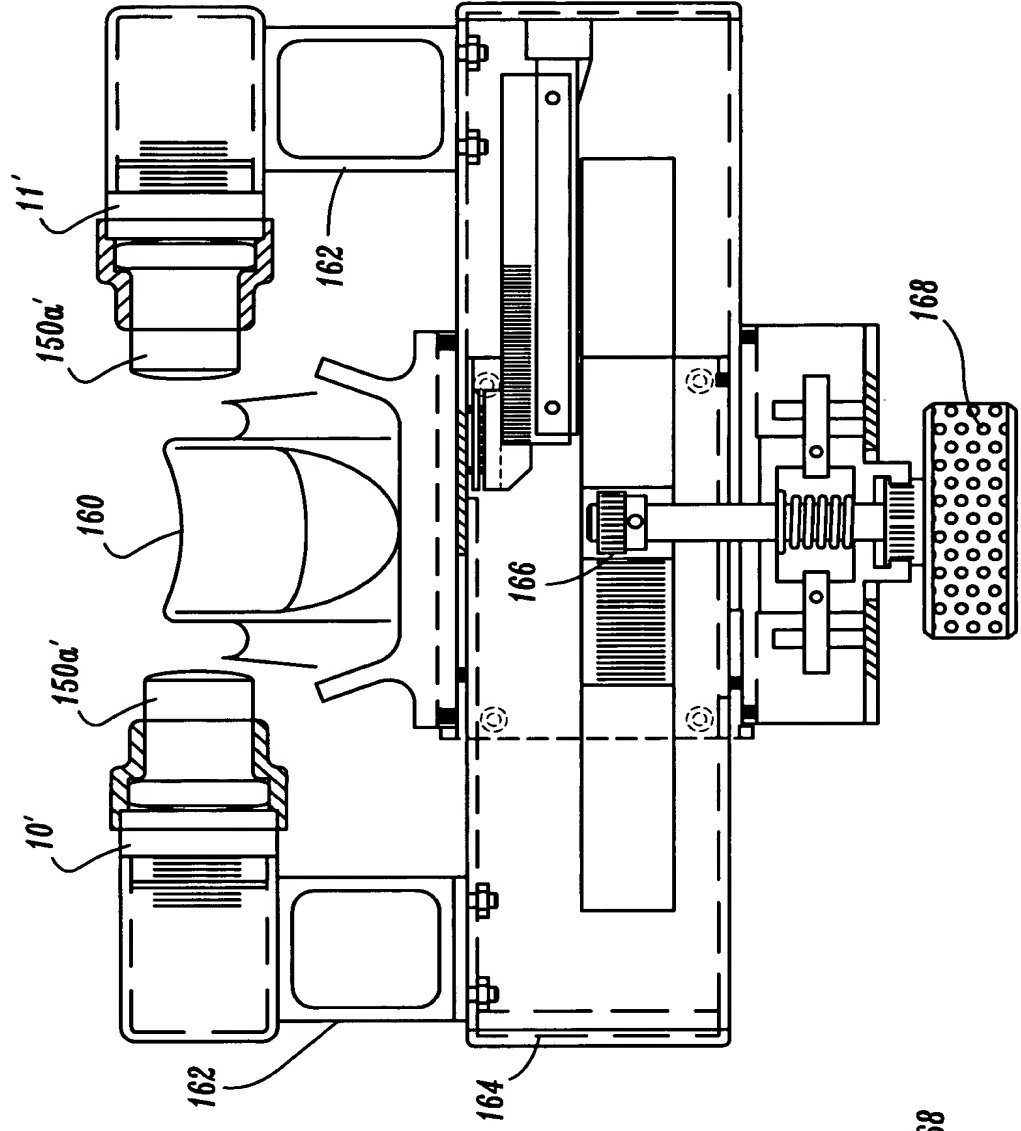


FIG. 15A

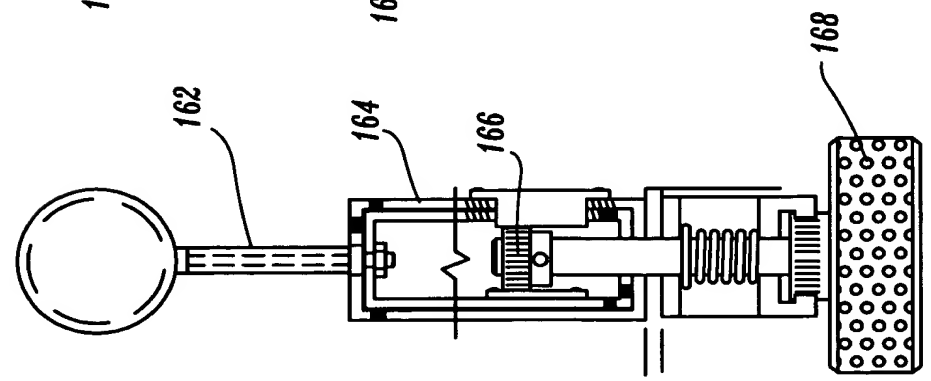


FIG. 15B

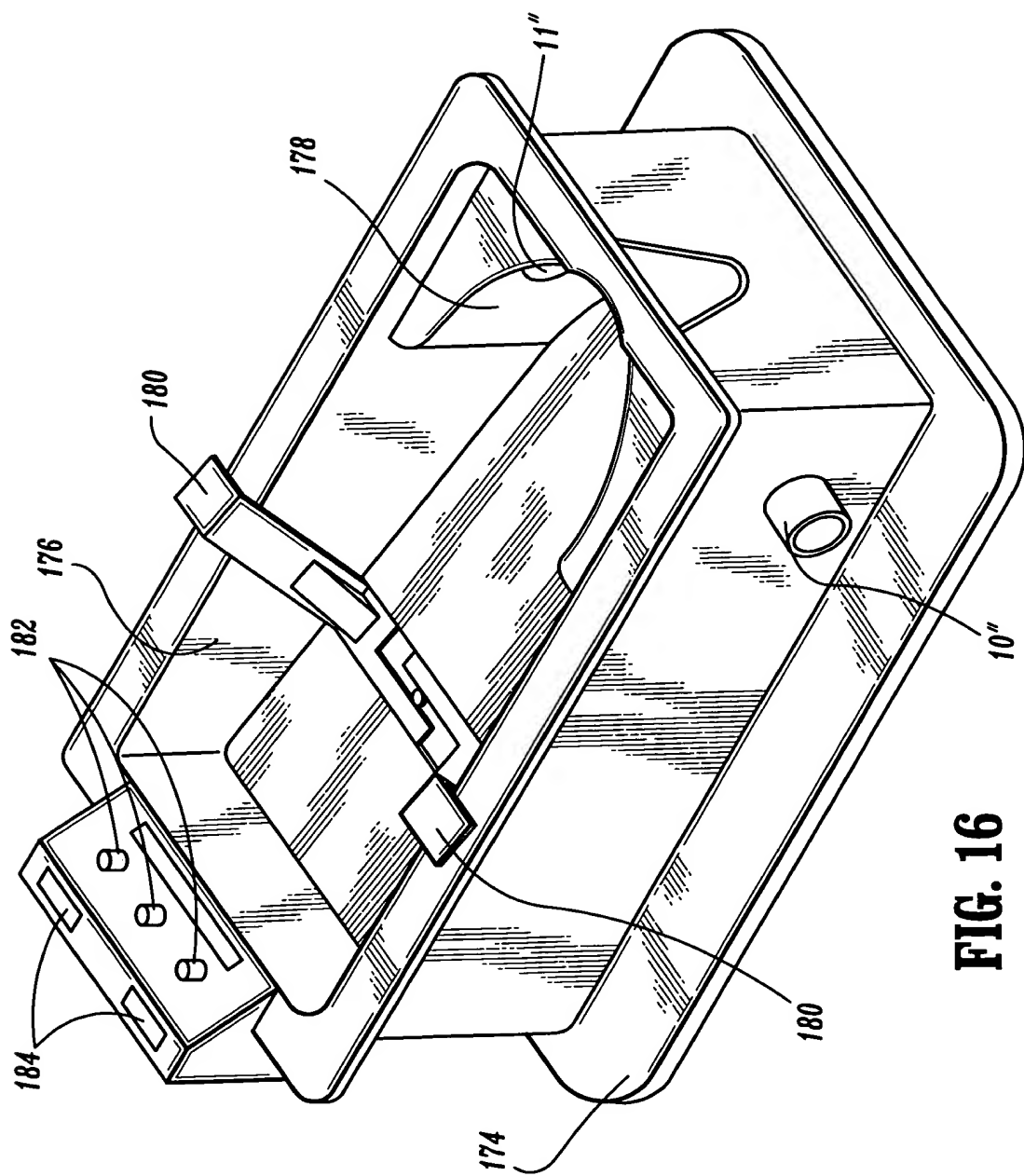


FIG. 16

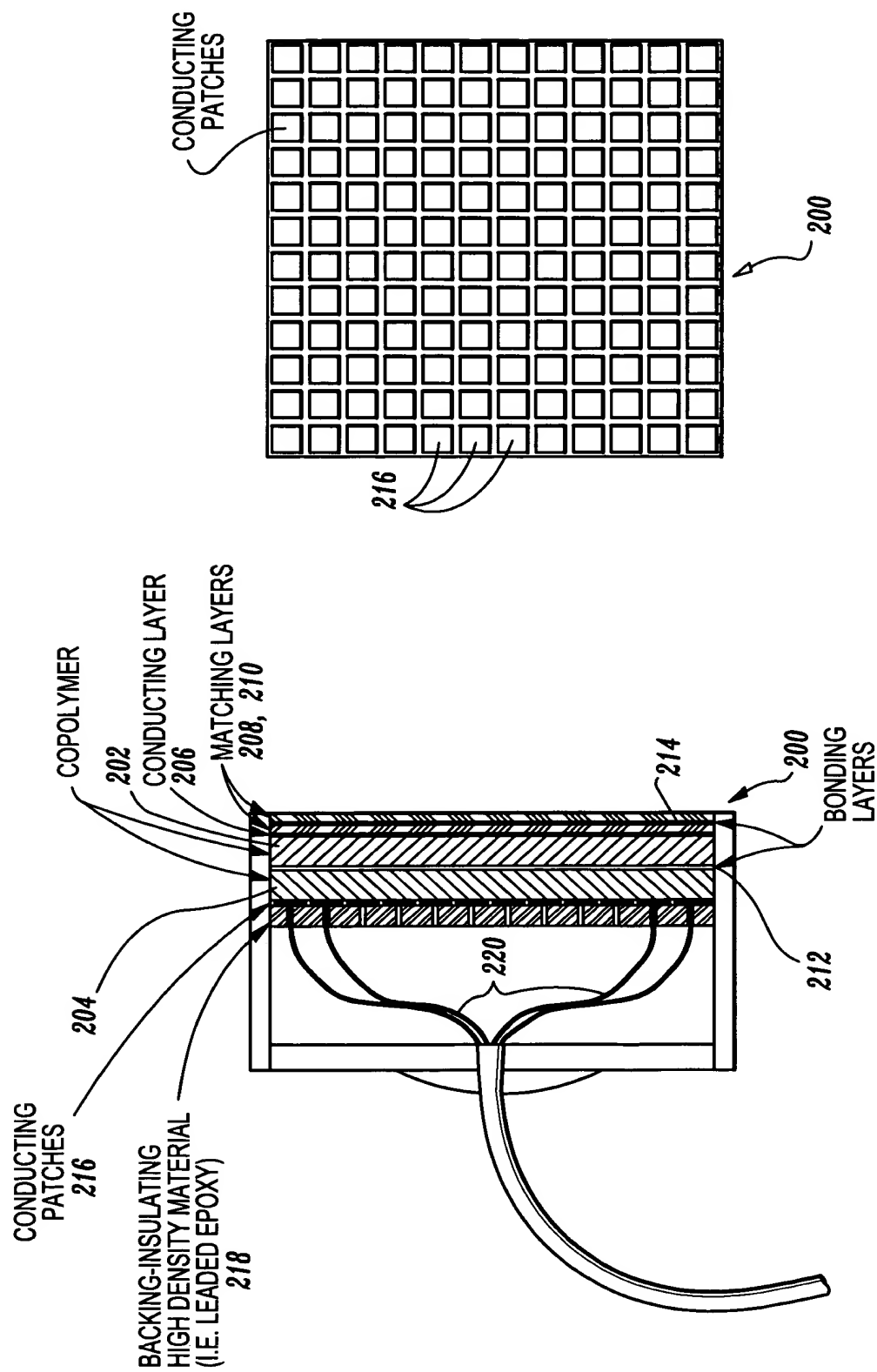


FIG. 17A

FIG. 17B

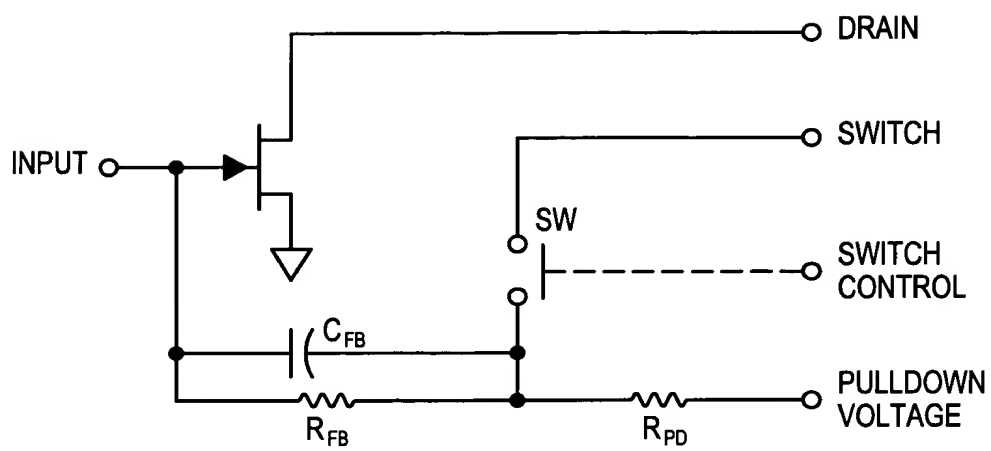


FIG. 18A-N-Channel FET Input Stage

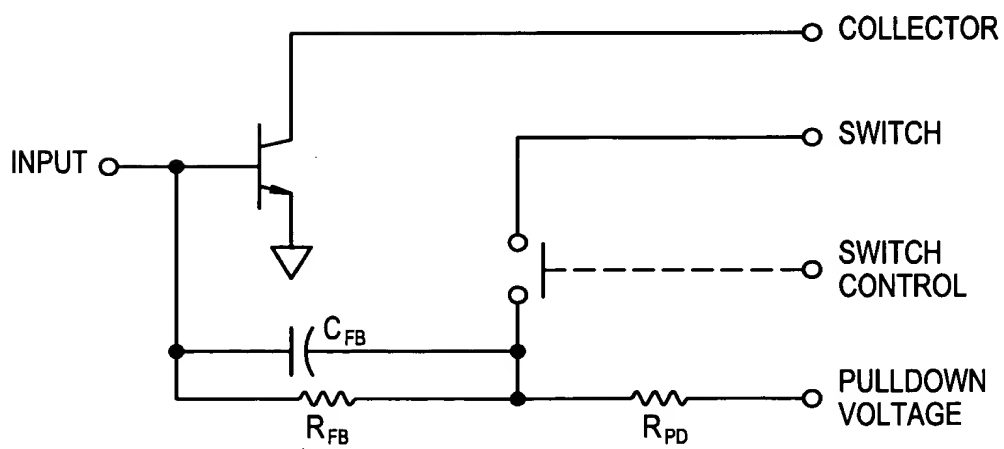


FIG. 18B-NPN Transistor Input Stage

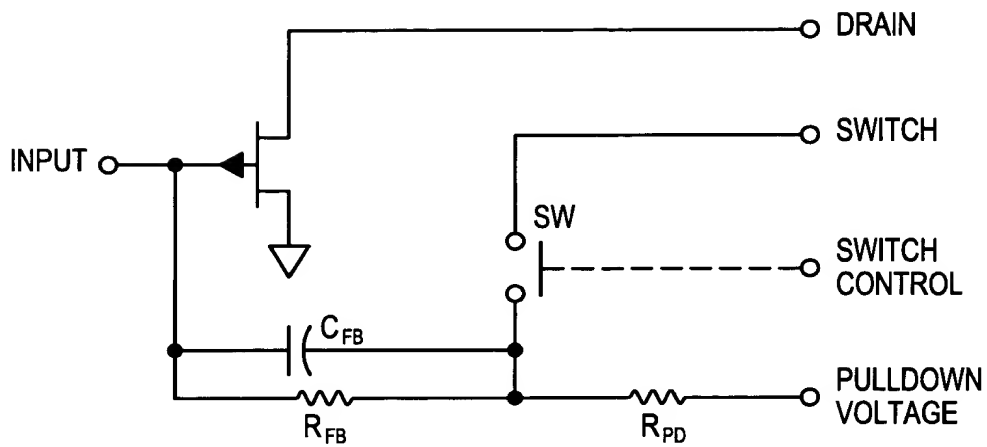


FIG. 18C- P-Channel FET Input Stage

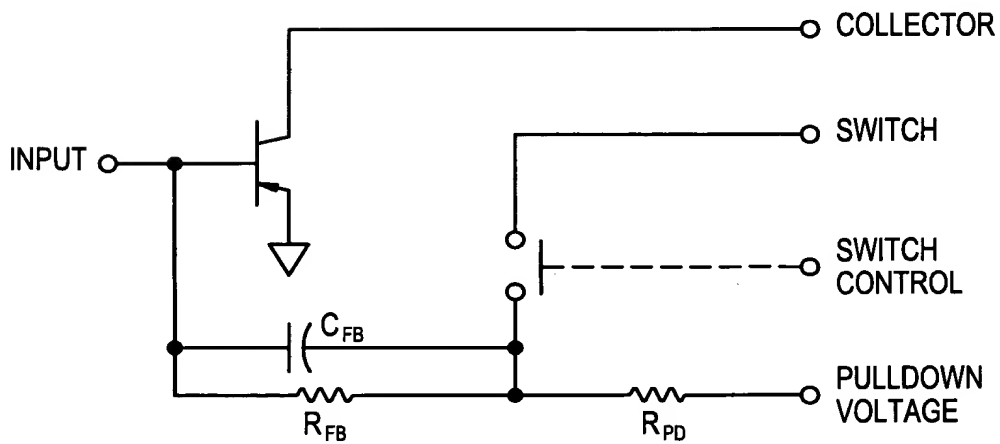


FIG. 18D- PNP Transistor Input Stage

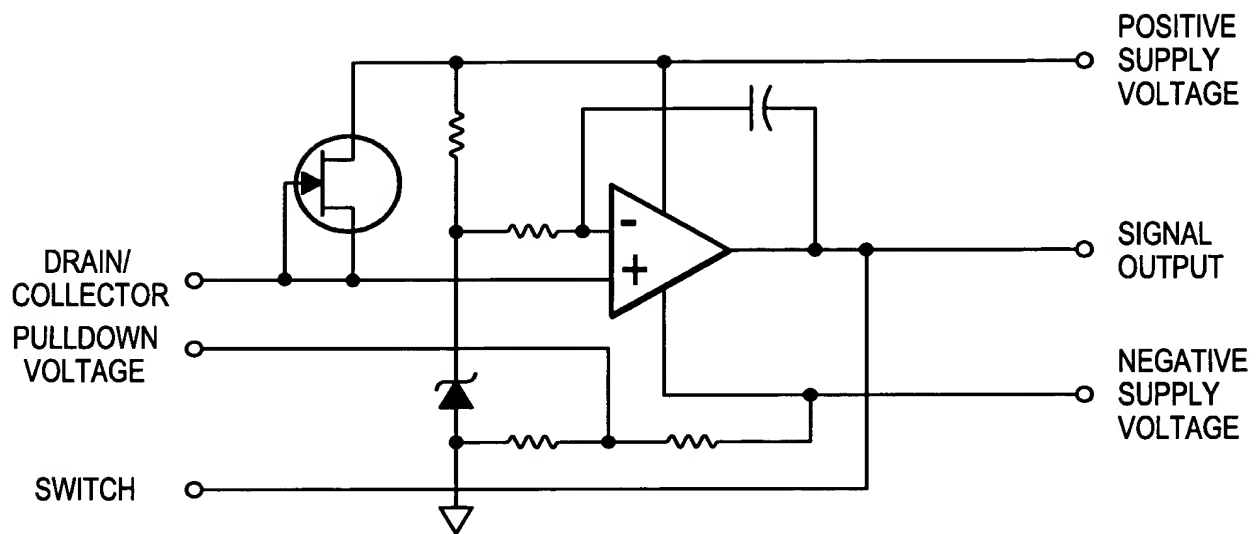


FIG. 18E- PREAMPLIFIER OUTPUT STAGE
For N-CHANNEL OR NPN INPUT STAGE

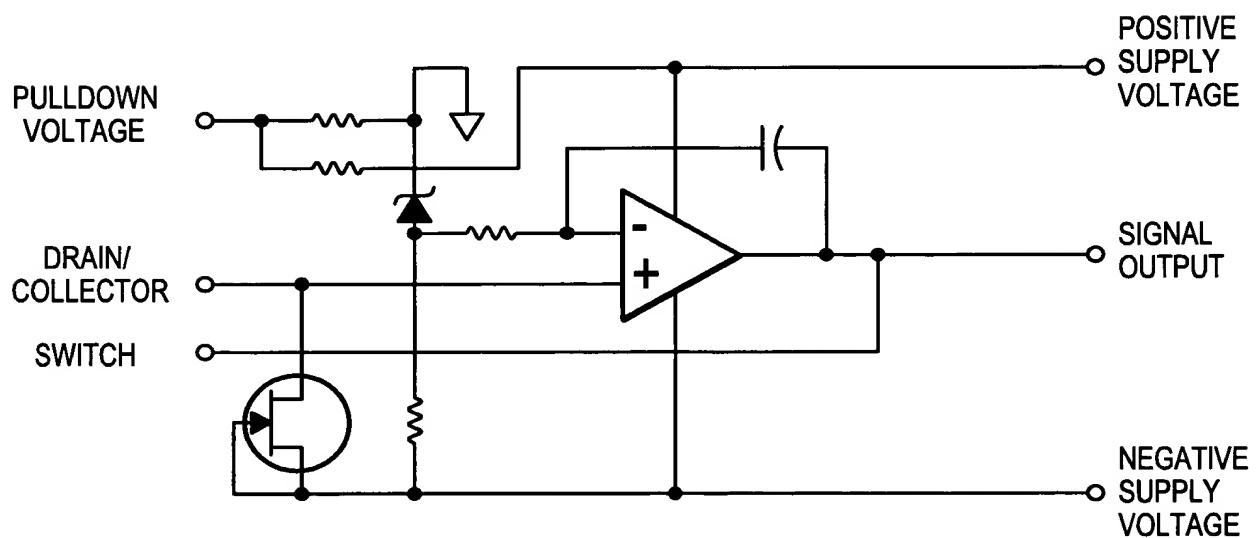


FIG. 18F- PREAMPLIFIER OUTPUT STAGE
For P-CHANNEL OR PNP INPUT STAGE